



oneAPI DevSummit 2022



oneAPI

Agenda Page

Change the
Time Zone

oneAPI DevSummit at IWOCL 2022

Time zone: (UTC-08:00) Pacific Time (US)

Monday, May 9, 2022

Time	Presentations	Actions
07:00 a.m. - 07:15 a.m.	Introduction Speaker(s): Sujata Tibrewala View More	ADD TO CALENDAR →
07:15 a.m. - 07:45 a.m.	Parallel programming models and SYCL in the UK's Exascale programs Speaker(s): Simon McIntosh View More	ADD TO CALENDAR →
07:45 a.m. - 08:15 a.m.	Learning SYCL: challenges and opportunities Speaker(s): Roberto Di Remigio Eikås View More	ADD TO CALENDAR →
08:30 a.m. - 09:00 a.m.	TAU Performance System for SYCL and E4S Speaker(s): Sameer Shende View More	ADD TO CALENDAR →
09:00 a.m. - 09:30 a.m.	Challenges Faced When Porting Ginkgo to the SYCL Ecosystem Speaker(s): Yu-hsiang Tsai View More	ADD TO CALENDAR →
09:45 a.m. - 10:30 a.m.	Developing for the next generation of RISC-V processors with SYCL Speaker(s): Raja Appuswamy, Michael Wong, Hideki Sugimoto, Zdenek Priky, Mark Himmelstein View More	ADD TO CALENDAR →
10:30 a.m. - 10:45 a.m.	Conclusion Speaker(s): Russel R Beutler View More	ADD TO CALENDAR →

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Using the Console

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Speaker Video

Discord Link

Abstract and Biographies



Quick Links

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Badges
oneAPI DevSummit at IWOCL 2022

Badge Leaderboard

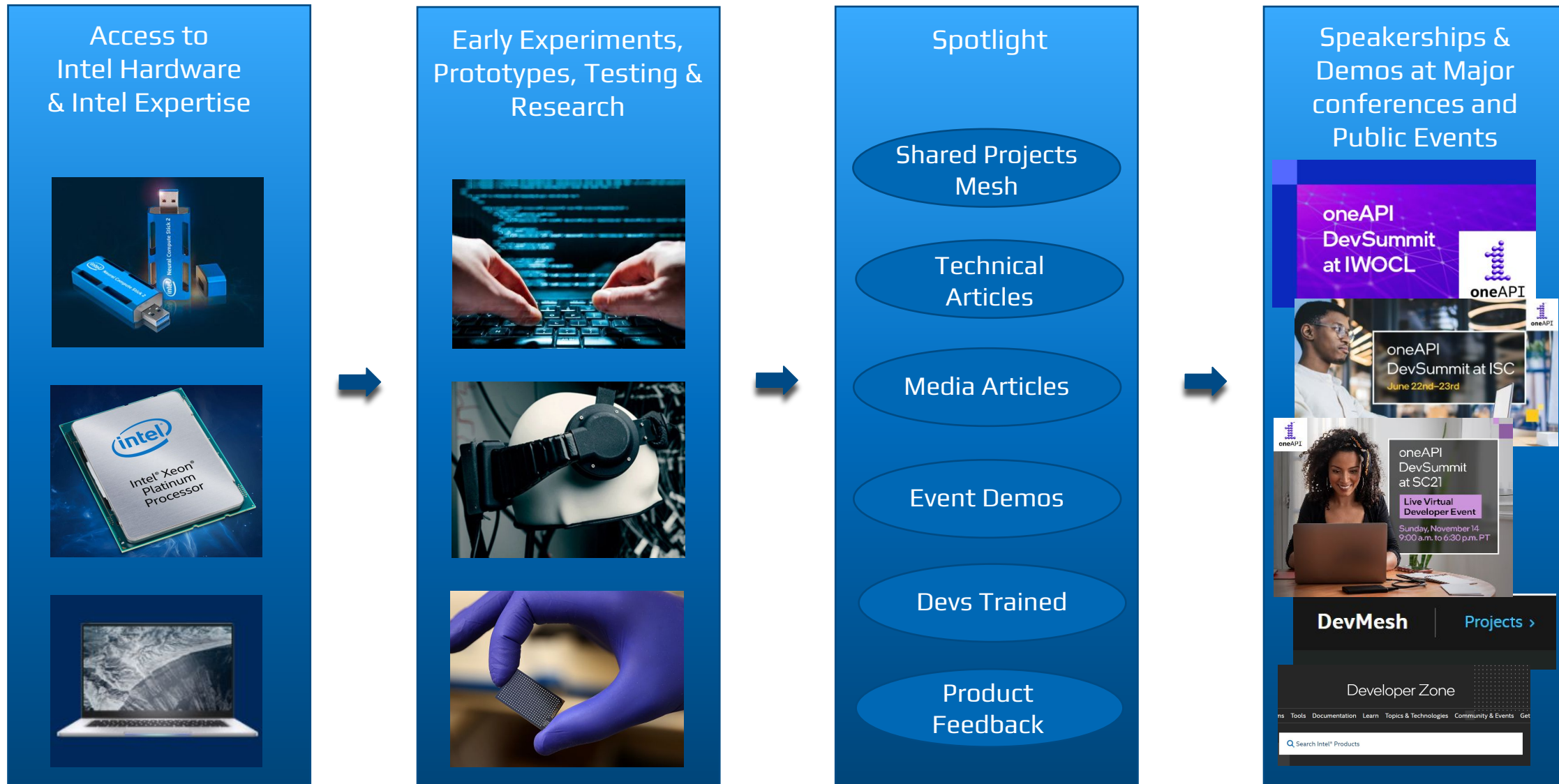
Badge List

- Session attendee champ!**
Attend at least 3 sessions to earn this badge.
- Collaboration hero!**
Click on Discord to interact with other developers to earn this badge.
- Avid Reader!**
Visit the Resources area to find out more about oneAPI to earn this badge!
- Survey superstar!**
Go to Survey in the top navigation bar and complete the online survey to earn this badge.

Track who's
on top of the
badge
leaderboard

Receive
badges
after
completing
an action

Why Should You Join oneAPI Innovator Program?



DevSummit Innovator Presenters



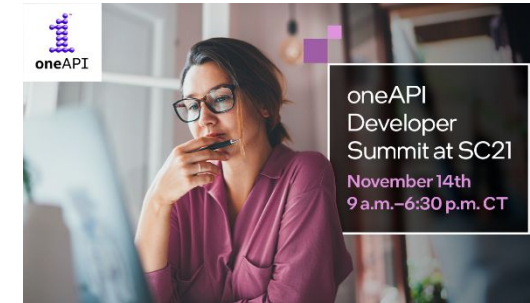
Aleksandar Ilic '20, '21
Alessandro Faria '20
Andrew Pastrello '21
Andrew Richards '20
David Hardy '20
Denisa Constantinescu '20
Hartwig Anzt '20
Ho Leung Ng '20
Luke Kljucaric '21
Raja Appuswamy '22
Rafael Asenjo '20
Ricardo Menotti '20
Sameer Shende '21, '22
Xiaozhu Meng '20



Aleksandar Ilic '21
Alexey Gokhberg '21
David Hardy '21
Hartwig Anzt '21
Istvan Reguly '22
Marcel Breyer '21
Yan Luo '21
Zheming Jin '21



Hartwig Anzt '21
Raja Appuswamy '21
Zhen Ju '21



Aleksandar Ilic '21
Alexey Gokhberg '21
Antimo Musone '21
Denisa Constantinescu '20
Istvan Reguly '21
Raja Appuswamy '21
Rafael Asenjo '21
Zheming Jin '21

ISC, SC, and Tutorial Presenters



Aleksandar Ilic '22 Workshop
Organizing Committee

Hartwig Anzt '21, '22,
Workshop Chair

Istvan Reguly '21

Raja Appuswamy '22
Workshop Organizing
Committee

Sameer Shende '21, '22



David Hardy, Booth

Sameer Shende, Review
Committee



Aleksandar Ilic, Europar Chair



Aleksandar Ilic '20
Ricardo Menotti '20

Innovator Podcasts/Blogs/Articles



Podcasts

Aleksandar Ilic
Denisa Constantinescu
Maura Tokay



Challenge Winners

Andrew Pastrello
Raja Appuswamy
Zhen Ju



Blogs

Denisa Constantinescu
Maura Tokay



Most Active Innovator

Alessandro Faria



Articles

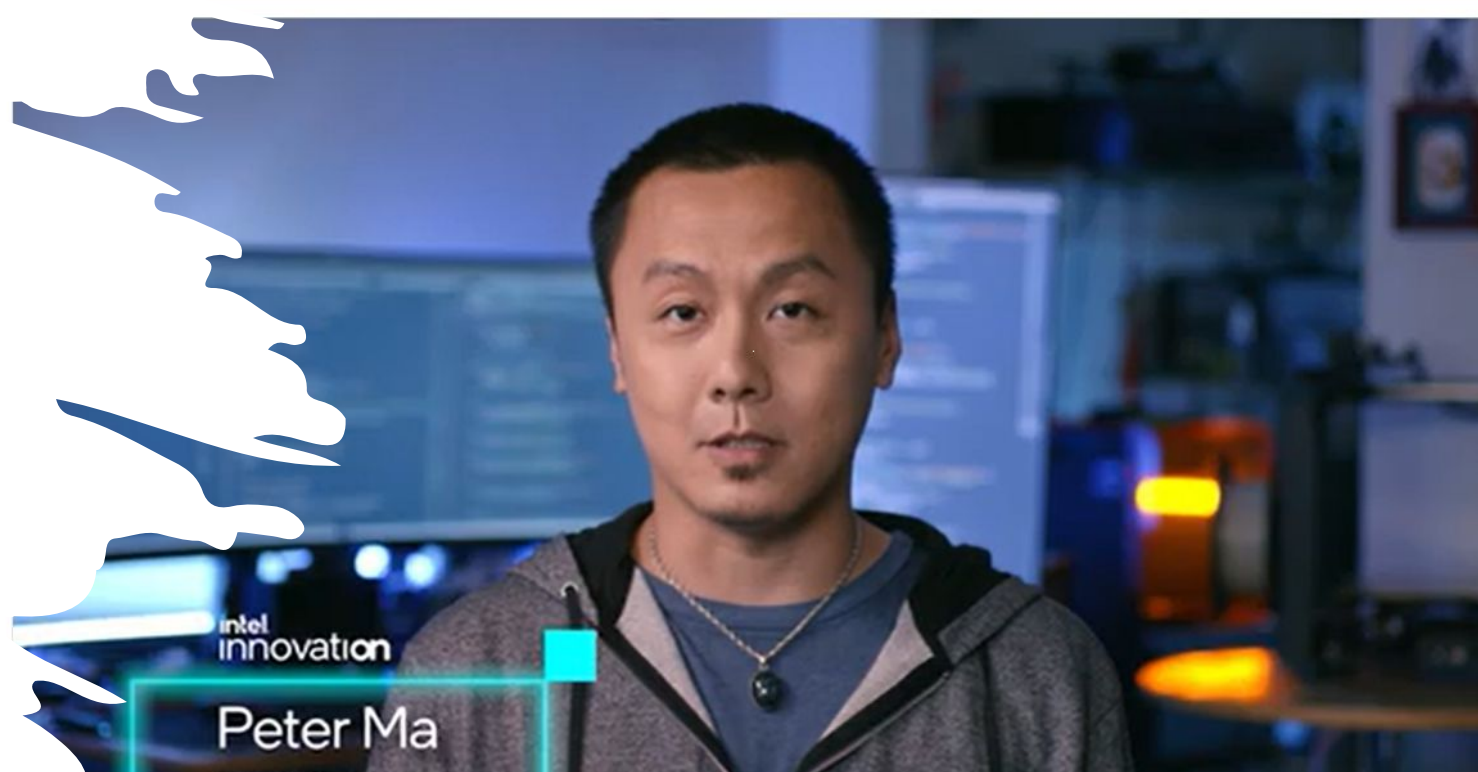
Denisa Constantinescu
Hartwig Anzt
Rafael Asenjo

Innovator Developer Story Videos

Check out Maura Tokay and Peter Ma's video on the Intel Innovation website where they highlight their developer journey and the story behind their passion!



Scan QR code





Recent Innovator Activity

- Abhishek Nandy published his first article on Open VKL called “Intel Open VKL Library”
- Innovator Tamer Mahmoud spoke at the High Performance Computing for Mathematics and Applications conference in Suez University where he did his presentation on “Optimization Strategy for Compute Intensive Systems - CFD case study.”
- David Hardy, Peter Ma and Maura Tokay featured in a Circuit article “Developers sound off on what they need from Intel, How developers across the U.S. are using Intel technology and what they need to succeed.”
- Hartwig Anzt published “A math library designed for platform portability”
- Alexey Gokhberg published an article “A Lean Approach to Embedded Deep Learning”
- Alessandro Faria received an award for 2021 12AI Professional of the Year
- Alexey Gokhberg, Denisa Constantinescu and Rafael Asenjo are featured on IEE Computer’s “Feedfoward”

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Announcements

- > Jun 9, 2021 ***New*** SSH Configuration Change is Required — A recent DNS change now requires users to u...
- > Mar 15, 2021 DevCloud Maintenance on March 25, 2021 — Intel DevCloud may be unavailable from 7:00 a...
- > Feb 1, 2021 Intel® Iris® Xe MAX GPU is now available — Intel is on a journey to bring the industry a redefine...

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The Intel DevCloud is a development sandbox to learn about programming cross architecture applications with OpenVino, High Level Design (HLD) tools – oneAPI, OpenCL, HLS – and RTL.

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To chat and ask questions...



Head to Discord!
#oneapidevsummit

<https://discord.com/invite/yFNXf48tvq>

DevSummit at IWOCL Speakers

Parallel programming models and SYCL in the UK's Exascale programs



**Simon
McIntosh-Smith**
University of
Bristol

TAU Performance System for SYCL and E4S



Sameer Shende
University of
Oregon

Learning SYCL: challenges and opportunities



**Roberto Di
Remigio Eikås**
ENCCS

Challenges Faced When Porting Ginkgo to the SYCL Ecosystem



**Yu-Hsiang
Mike Tsai**
Karlsruhe
Institute of
Technology

DevSummit at IWOCL Speakers

Developing for the next generation of RISC-V processors with SYCL



Raja Appuswamy
Eurecom



Mark Himelstein
RISC-V International



Zdenek Prikryl
Codasip



Michael Wong
Codeplay



Hideki Sugimoto
NSITEXE, Inc

oneAPI SYCL Essentials



Benjamin Odom
Intel



Praveen Kundurthy
Intel

oneAPI DevSummit at IWOCL 2022 Agenda Day 1

Presentation Name	Speaker(s)	Time (Central Time)
Introduction	Sujata Tibrewala	9:00 – 9:15 am
Parallel programming models and SYCL in the UK's Exascale programs	Simon McIntosh-Smith	9:15 – 9:45 am
Learning SYCL: Challenges and Opportunities	Roberto Di Remigio Eikås	9:45 – 10:15 am
Break		10:15 – 10:30 am
TAU Performance System for SYCL and E4S	Sameer Shende	10:30 – 11:00 am
Challenges Faced When Porting Ginkgo to the SYCL Ecosystem	Yu-Hsiang Mike Tsai	11:00 – 11:30 am
Break		11:30 – 11:45 am
Developing for the next generation of RISC-V processors with SYCL	Raja Appuswamy Mark Himmelstein Zdenek Prikryl Michael Wong Hideki Sugimoto	11:45 – 12:30 pm
Conclusion	Russel Beutler	12:30 – 12:45 pm
Happy Hour	Russel Beutler	12:45 – 1:45 pm

oneAPI DevSummit at IWOCL 2022 Agenda Day 2

Presentation Name	Speaker	Time (Central Time)
Introduction	Sujata Tibrewala	9:00 – 9:15 am
oneAPI SYCL Essentials	Benjamin Odom Praveen Kundurthy	9:15- 10:45 am
Conclusion	Russel Beutler	10:45- 11:00 am